

REMARKS

The Examiner's Final Office Action of April 7, 2003 has been received and its contents reviewed. Applicants would like to thank the Examiner for the consideration given to the above-identified application.

Claims 1-32 are pending in the present application, of which claims 1, 6, 11, 14, 17, and 22 are independent. By this Amendment, claims 1, 6, 11, 14, 17, and 22 have been amended. In view of these actions and the following remarks, reconsideration of this application is now requested.

Referring to the detailed Office Action, claims 1, 2, 4-7, 9-18, 20-23, 25, and 26 stand rejected under 35 U.S.C. §103(a) as unpatentable over Sawada (U.S. Patent No. 6,078,317 – hereafter Sawada). Further, dependent claims 3, 8, 19, and 24 stand rejected under 35 U.S.C. §103(a) as unpatentable over Sawada in view of Jeong (U.S. Patent No. 6,008,801).

In response to the rejections, independent claims 1, 6, 11, and 14 have been amended so as to include a feature of a digital video signal dividing circuit which is fed with an image signal processed by an image signal processing circuit. The digital video signal dividing circuit is also recited in independent claims 17 and 22. The amended feature is supported at least in, e.g., Fig. 1, and in page 7, lines 20-21 of the specification.

Further, claims 17 and 22 have been amended so as to include a feature that each circuit is formed over the same substrate as a pixel portion, which is supported at least in, e.g., Fig. 1 and in page 5, lines 24-26 of the specification.

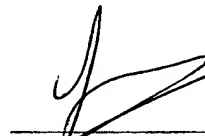
Applicants respectfully submit that Examiner appears to have failed to address the digital video signal dividing circuit recited in claims 17 and 22. Further, it appears that Sawada fails to teach the display panel comprising the digital video signal dividing circuit.

The requirements for establishing a *prima facie* case of obviousness, as detailed in MPEP § 2143 - 2143.03 (pages 2100-122 - 2100-136), are: first, there must be some suggestion or motivation, either in the reference themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference to combine the teachings; second, there must be a reasonable expectation of success; and, finally, the prior art reference (or references when combined) must teach or suggest all of the claim limitations. At least for the reason that the cited prior art references fail to teach, disclose, or suggest digital video signal dividing circuit, the §103(a) rejections are improper.

In view of the amendment and argument set forth above, Applicants respectfully requests reconsideration and withdrawal of the pending §§ 103(a) rejections.

While the present application is now believed to be in condition for allowance, should the Examiner find some issue to remain unresolved, or should any new issues arise which could be eliminated through discussions with Applicants' representative, then the Examiner is invited to contact the undersigned by telephone in order that the further prosecution of this application can thereby be expedited.

Respectfully submitted,



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